



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshinori Hino et al.

Serial No. : 10/080,831

Filed : February 21, 2002

Title : SEMICONDUCTOR DEVICE AND PATTERN LAYOUT METHOD THEREOF

Art Unit : 2815

Examiner : J. Nguyen

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF MARCH 9, 2004

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

July 8, 2004

Date of Deposit

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